



HH
COPY OF PAPERS
ORIGINALLY FILED

03C05
OFGS File No.: P/1071-1451

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Kazuya Sayanagi, et al.

Date: December 18, 2001

Serial No.: 09/974,668

Group Art Unit: --

Filed: October 10, 2001

Examiner: --

For: HIGH-FREQUENCY CIRCUIT BOARD UNIT, HIGH-FREQUENCY MODULE USING
THE SAME UNIT, ELECTRONIC APPARATUS USING THE SAME MODULE, AND
MANUFACTURING METHOD FOR THE HIGH-FREQUENCY CIRCUIT BOARD UNIT

Customer Service
Asst. Commissioner of Patents and Trademarks
Washington, DC 20231

RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Sir:

The attached Notice, issued November 14 2001, contains a requirement to file formal drawings. Attached are 6 sheets of formal drawings containing Figs. 1 through 11 for the above identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on December 18, 2001

Respectfully submitted,

James A. Finder
Name of applicant, assignee or
Registered Representative

Signature

December 18, 2001

Date of Signature

James A. Finder
Registration No.: 30,173
OSTROLENK, FABER, GERB & SOFFEN, LLP
1180 Avenue of the Americas
New York, New York 10036-8403
Telephone: (212) 382-0700

JAF:sam